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Dr. Chiu joined IBM as a research staff member at the Thomas J. Watson Research Center in 1974. He has been working on system measurement, data analysis, modeling, and performance evaluation. He is currently the manager of MVS restructure in the Experimental Systems Laboratory of the Computer Science Department. Dr. Chiu received his Ph.D. degree in electrical engineering and computer sciences from the University of California, Santa Barbara, in 1973.

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Mr. Gravina is a senior systems engineer in IBM's Banking Branch in the City of London. He joined IBM in 1968, having spent ten years in the engineering industry, and has since worked on many aspects of large data processing systems. Mr. Gravina received his B.Sc. in electrical engineering from Imperial College, London.

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Dr. Kraemer joined the System Performance Analysis Department at Boeblingen in May 1978. Prior to joining IBM, he was a Scientific Assistant at the Institute for Switching Techniques and Data Processing at the University of Stuttgart, Germany from 1970, where he worked in the area of queuing theory for computers and switching systems. From January 1975 to March 1976 he held a Post-Doctoral Fellowship with the IBM laboratories in Boeblingen. During that time he worked on computer system modeling and on the development of performance tools for systems analysis and design. Since that time, he has been a lecturer on these topics for IBM Boeblingen. Dr. Kraemer received his degrees in electrical engineering, including the Dr.-Ing. degree in February 1975. He is a member of the Society for Electrical Engineers in Germany (VDE) and the Telecommunications Society (NTG).

J. Paul Morrison

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Mr. Morrison is an advisory development analyst in industry application design. He joined IBM United Kingdom in 1959 and worked for five years on IBM 650 and 1400-series applications. Moving to Poughkeepsie, New York, he worked in compiler design and, later, in programming language strategy. He then joined the engineering information system team and helped design a data-base access method, from which many concepts were subsequently incorporated into VSAM. Mr. Morrison transferred to IBM Canada in 1968, joining the Montreal Data Centre as a programmer. It was there that he developed the AMPS concept (the DSLM prototype). Since then he has been active in application development methodolo-

gies. From 1971 to 1976 he helped a customer in Montreal design and install a large real-time system, and in 1976 he moved to the Toronto Laboratory, where he has been involved in software systems design, with special reference to problems of software support for users of languages other than English. Mr. Morrison was educated at Eton College in England and received his M.A. in archaeology and anthropology from the University of Cambridge in 1962.

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As a member of the MIT Project MAC, Dr. Scherr did research on time-sharing system performance and user characteristics. After joining IBM in 1965, he participated in the definition of the System/370 architecture, and in 1968 he became the design manager for the TSO Project. In 1971, Dr. Scherr was given overall project responsibility for the first MVS release (OS/VS2 Release 2), and he subsequently became the systems manager for both OS/360 and OS/VS2. In 1977, he was assigned to manage the design of a new programming system in support of distributed processing, and he is now the manager of distributed systems programming. Dr. Scherr received the Ph.D. degree in electrical engineering and computer science from the Massachusetts Institute of Technology in 1965.

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