d i g i t a l EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

Engineering Change Order Log
DEC-O-LOG

DA28-F
Interprocessor Buffer, PDP-11 To Positive Bus

PROCESSOR TYPE   DECsystem-10, PDP-11

DA28F-B0001   CODE: DF   DD: A   WL: A
SEP-73 - PROBLEM 1: CONNECT signal not working properly.
PROBLEM 2: COMMON REQUEST signal not working properly.
PROBLEM 3: JAM SWITCH misconfigured.
PROBLEM 4: WCOF signal causing software bogs.
PROBLEM 5: JAM works unreliably.
PROBLEM 6: JAM delays need readjusting.
PROBLEM 7: Synchronization of processors incorrect.
CORRECTIONS 1 thru 7: These problems are corrected by the addition of an M133 module in slot C14, an M246 in slot C13, a #13-00365 1K ohm, 1/4 watt, 5% resistor, eighty-one wiring add's, and forty-seven wiring delete's.

NOTE: This FCO cannot be installed and checked in sections.
In-plant effectivity -Retrofit all DA28-F's
Field effectivity -Retrofit all DA28-F's
(Time To Install And Test 9.0 Hours.) (Kit Contents -F1067 -FCO/Prints And Parts)

DA28F-B0002   CODE: F   DD: B   WL: B
JAN-74 - PROBLEM 1: JAM option does not work reliably with PDP-11/40 or PDP-11/45 CPU's.
CORRECTION 1: Correct JAM timing by wiring changes as follows: DE-LETE A14C2 to A14T1, B14C2 to B14C1, B14P1 to B14R1, B14C1 to B14P1, B14C2 to A14T1, and B14V1 to B14J1; ADD B14D2 to B14P1, B14P1 to B14R1, B14C1 to B14C2, B14C1 to B14D2, A14C2 to A14T1,A14T1 to B14C2, B14V1 to C13L1, C13K2 to B14J1, C13M2 to C13K1, C13N2 to C13M2, and C12K1 to C13J1.
PROBLEM 2: JAM DC LO L should be JAM DC LO H , among other print errors.
CORRECTION 2: Correct print C-BS-DA28-F-J and BS prints -D, -BT , and -DR11 .
In-plant effectivity -Retrofit all units
Field effectivity -Retrofit all DA28-F's
(Time To Install And Test 1.8 Hours.) (Kit Contents -NF1157 - FCO/Prints)