

CONTENTS

- 13-010 Introduction
- 13-020 Removal and Replacement
- 13-030 Panel Layout and Indicator Decal

13-010 INTRODUCTION

Operation of the CE panel and CE subpanel switches and indicators is described in the *IBM System/34 5340 System Unit Theory Diagrams Manual, SY31-0458*. The locations of parts on the back of the CE panel and CE subpanel are shown in Volume D (*Field Service Logics*). A list of the signal wire names and the connector pins to which they are connected is also given.

13-020 REMOVAL AND REPLACEMENT

To remove the CE panel:

1. Set Power to O (operator panel).
2. Remove the three hexagonal bolts **A** that hold the CE panel to the machine frame.
3. While holding the CE panel in your hand, disconnect the three signal cables.

To reinstall the CE panel:

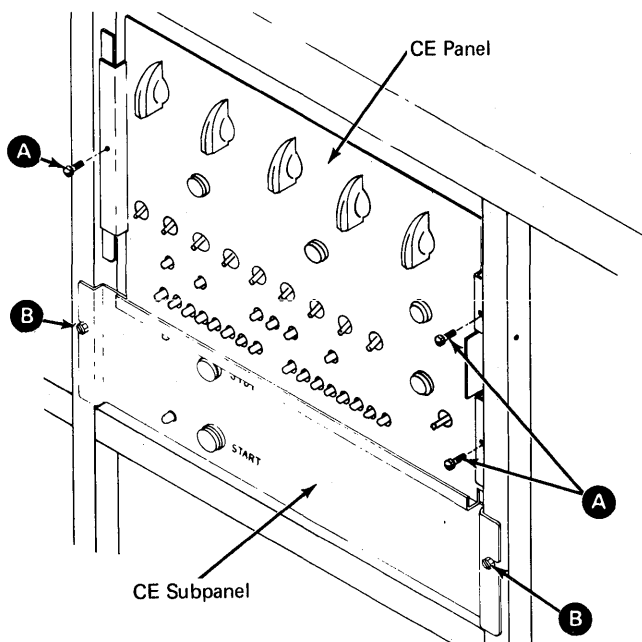
1. Reconnect the signal cables to the same sockets they were disconnected from. See Volume D (FSL) page CE160 to identify the cable connectors on the CE panel.
2. Reinstall the three hexagonal bolts **A** that hold the CE panel to the machine frame.

To remove the CE subpanel:

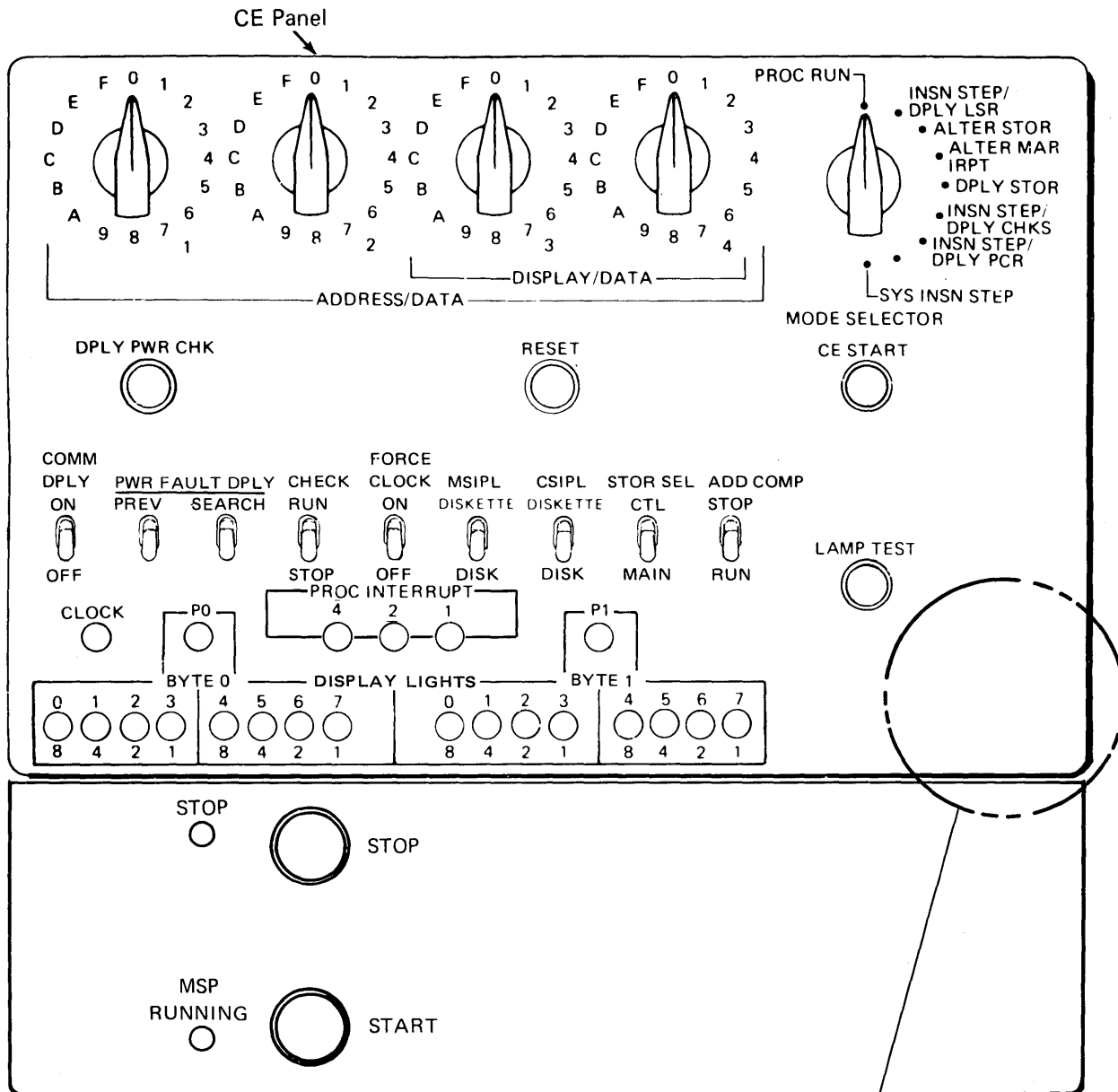
1. Set Power to O (operator panel).
2. Remove the two hexagonal bolts **B** that hold the CE subpanel to the machine frame.

To reinstall the CE subpanel:

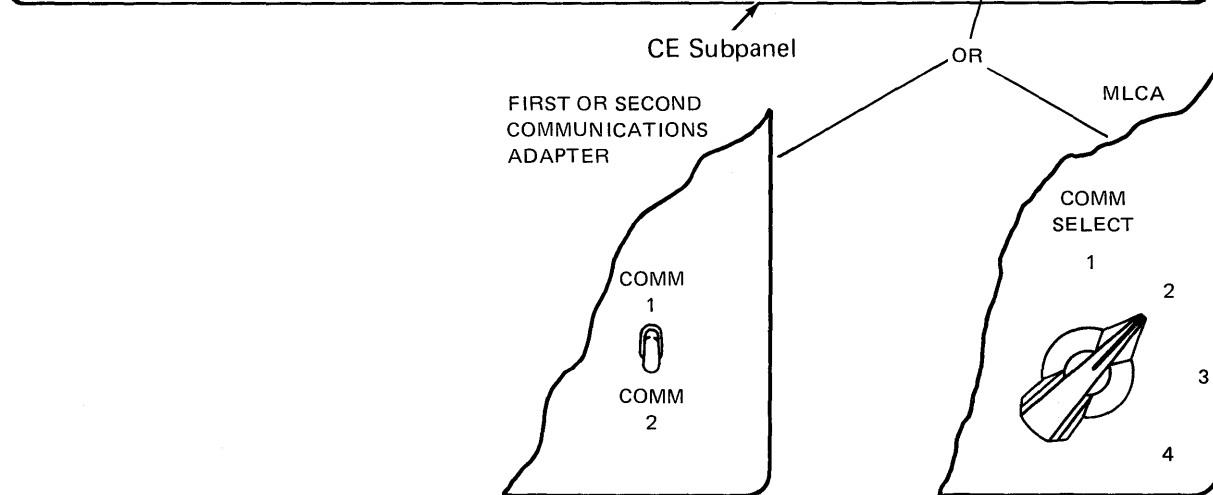
Reinstall the two hexagonal bolts **B** that hold the CE subpanel to the machine frame.



13-030 PANEL LAYOUT AND INDICATOR DECAL



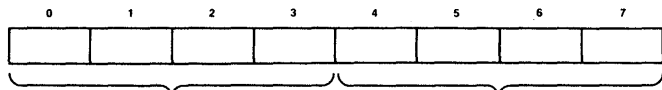
13



EVENT INDICATOR (BYTE 0)

LIGHT OFF
 LOAD - DATA TRANSFER COMPLETE NO DATA CHECK
 P - ADAPTER RECEIVED IMPL SIGNAL AND INITIATED ACTION IN RESPONSE
 0 - FIRST CYCLE STEAL REQUEST RECEIVED DATA TRANSFER HAS STARTED
 1 - DATA TRANSFER OF 4096 BYTES HAS COMPLETED
 2-7 - TURNED OFF WITH MICRO INSTRUCTIONS

POWER FAULT DISPLAY (BYTE 0)



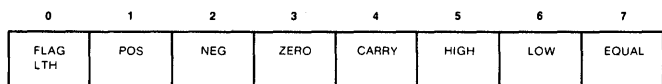
X0 * X1 ADTL SENSE CD MISSING X2 OV - ADDITIONAL FAULT X3 NOT USED X4 OC - ADDITIONAL FAULT X5 NOT USED X6 UV - ADDITIONAL FAULT X7 * X8 NOT USED X9 SENSE CD MISSING XA OV - PRIMARY FAULT XB NOT USED XC OC - PRIMARY FAULT XD NOT USED XE UV - PRIMARY FAULT XF *	X0 * X1 -5V BASE X2 -8.5V BASE X3 -24V BASE X4 -24V BASE X5 -6V BASE REGULATOR X6 -4V BASE REGULATOR X7 -5V BASE REGULATOR X8 -12V FEAT A X9 -12V FEAT A OR FEAT REG CARD OR SENSE CD MISSING XA -5V FEAT B OR SENSE CD MISSING XB FEATURE POWER SUPPLY C XC FEATURE POWER SUPPLY D XD RESERVED XE RESERVED XF FEATURE POWER SUPPLY G
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• SPECIAL

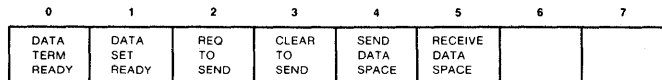
X'00' IS NO FAULT SINCE AC RESTORED
 X'7F' IS SEARCH COMPLETE
 X'FF' DISPLAY W THERMAL CHECK WHEN POWER WAS ON
 X'E1' DISPLAY W THERMAL CHECK WHEN POWER WAS OFF

- ADD COMP SYNC (SYNC)	AA1-K2-D12	
- T3 AND PHASE A (SYNC)	AA1-G2-S12	
- CARRY IN (TIE DOWN)	AA1-G2-B11	

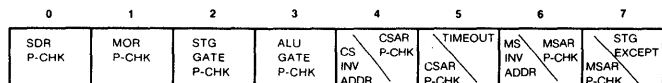
PROCESSOR CONDITION REGISTER (DPLY PCR) (BYTE 0)



COMM (BYTE 0)



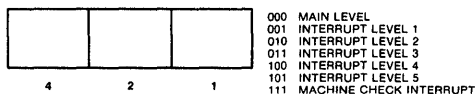
PROCESSOR CHECK DISPLAY (DPLY CHKS) (BYTE 0)



LOCAL STORE REGISTERS

	0	15	
MAIN LEVEL OR MACH CHECK 0 MAR MAB STACK 1 INTERRUPT 1 INTERRUPT 2 INTERRUPT 3 MAR MAB STACK 2 INTERRUPT 4 INTERRUPT 5	WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3 MAR MAB MAR (MC) MAB (MC) MAR 1 MAB 1 MAR 2 MAB 2 WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3 WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3 WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3 MAR 3 MAB 3 Reserved Reserved MAR 4 MAB 4 MAR 5 MAB 5 WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3 WR0 WR1 WR2 WR3 WR4 CS0 WR5 CS1 WR6 CS2 WR7 CS3	00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F 10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F 20 21 22 23 24 25 26 27 28 29 2A 2B 2C 2D 2E 2F 30 31 32 33 34 35 36 37 38 39 3A 3B 3C 3D 3E 3F	DATA SW 3-4

PROC INTERRUPT



PORT CHECK DISPLAY (DPLY CHKS) (BYTE 1)

